



ocket No.: 067471-0017

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: TAKAYAMA, SHUICHI, et al.

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE

Application No.: 10/051,335

Customer No.: 20277

Filed: October 19, 2001

Confirmation No.: 9991

Group Art Unit: 2191

Examiner: Satish Rampuria

Title: PROCESSOR FOR EXECUTING INSTRUCTIONS IN UNITS THAT ARE UNRELATED TO THE UNITS IN WHICH INSTRUCTIONS ARE READ, AND A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER, A LINKER, A DEBUGGER AND A DISASSEMBLER FOR SUCH PROCESSOR

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated July 1, 2005, having a three-month shortened statutory period for response set to expire on October 1, 2005, reconsideration of the above-identified application is respectfully requested in view of the following remarks.